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(2 AND 13).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0
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<u>L16</u>	L13 and l2	0	<u>L16</u>
<u>L15</u>	L13 and l1	12	<u>L15</u>
<u>L14</u>	L13 and l7	0	<u>L14</u>
<u>L13</u>	(mask near5 data) same (pattern near5 data)	2304	<u>L13</u>
<u>L12</u>	L10 and (pattern near5 data)	1	<u>L12</u>
<u>L11</u>	L10 and ((pattern near5 data) same (mask near5 data))	0	<u>L11</u>
<u>L10</u>	(ethernet near5 MAC) same (memory near5 device\$1)	9	<u>L10</u>
<u>L9</u>	L8 and (mask\$ same pattern\$)	2	<u>L9</u>
<u>L8</u>	L7 and (PCI near5 interfac\$)	40	<u>L8</u>
<u>L7</u>	(memory near5 device\$1) and l2	420	<u>L7</u>
<u>L6</u>	L5 and l1	1	<u>L6</u>
<u>L5</u>	l4 and ((second near5 stor\$) same (pattern\$1))	1187	<u>L5</u>
<u>L4</u>	((first near5 stor\$) same (pattern\$1))	2338	<u>L4</u>
<u>L3</u>	L2 and ((first near5 stor\$) same (pattern\$1))	0	<u>L3</u>
<u>L2</u>	L1 and (ethernet near5 card\$1)	676	<u>L2</u>
<u>L1</u>	(network\$ near5 interfac\$) near5 card\$1	4217	<u>L1</u>

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